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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/586,425

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EXAMINER

WILSON, ADRIAN S

ART UNIT

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2835

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PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary	Application No. 10/586,425	Applicant(s) LANG ET AL.	
	Examiner ADRIAN S. WILSON	Art Unit 2835	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 17 February 2009.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-15 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-15 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Reply Under 37 CFR 1.111

1. The Amendments and Applicant Arguments submitted on 02/17/2009 have been received and its contents have been carefully considered. The examiner wishes to thank the Applicant for the response to the Examiner's action.
2. Claims 1-15 are pending in this application. Claim 1, as currently amended, is presented for examination. Claims 2, 4-5, 7-8, 11 and 13-14, as originally submitted, are presented for examination. Claims 3, 6, 9-10 and 12 have been cancelled. Claim 15 is new.

Claim Rejections - 35 USC § 103

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

4. Claims 1-2, 4 and 10 are rejected under 35 U.S.C. 103(a) as being unpatentable over Shimada (Japanese Patent 08046381 A) (from IDS) in view of Chalberg et al. (US Patent 7,191,998 B1).
5. In re Claim 1, Shimada discloses a mounting plate (100) for electronic components (GTR) having coolant lines (12, 14, 16, 18) integrated in a plate body (10) for a cooling fluid to flow through, wherein a fastening arrangement for mounting electronic components to be cooled is arranged on the plate body (10), the fastening arrangement has at least one first groove (GP), having a C-shaped cross section and

Art Unit: 2835

extends in a straight line in the an extension direction of the mounting plate (100), into which at least one screw nut (M1-M4) for forming a screw (N1-N4) connection with an electronic component (GTR) can be inserted and fixed against relative rotation, and the fastening arrangement has at least one second groove (GP) designed identically to the first groove (GP) and extending parallel with the first groove (GP) with a distance from the first groove (GP) being substantially determined by the length of an extension perpendicularly with respect to the first and second grooves (GP) of the electronic component (GTR) to be mounted, the mounting plate (100) comprising: the electronic components (GTR) to be mounted have having screw holes at a distance from each other that is less than the distance of the second groove (GP) from the first groove (GP), and the electronic components (GTR) to be mounted are clampingly fixed in place at least on one side through an angle bracket by screws (N1-N4) within screw nuts (M1-M4) introduced into the respective groove (GP). Shimada does not explicitly disclose an angle bracket with elongated holes. However, the use of brackets with elongated holes to secure electrical devices is common in the art of electronics. For example, Chalberg discloses an angle bracket 30 that has an elongated hole 33 perpendicular to an extension direction for mounting an electronic device. It would have been obvious to one having ordinary skill in the art of electronics at the time the invention was made to have combined the bracket as disclosed in Chalberg with the mounting plate as disclosed by Shimada. All the claimed elements were known in the prior art and one skilled in the art could have combined the elements as claimed by known methods with no change in their respective functions, and the combination would have yielded

Art Unit: 2835

predictable results to one of ordinary skill in the art at the time of the invention. See *KSR International Co. v. Teleflex Inc.*, 550 U.S. ____, 82 USPQ2d 1385 (2007). The use of a bracket with elongated holes would allow for multiple mounting configurations.

6. In re Claim 2, Shimada discloses a fastening arrangement that has at least one further groove (GP), identical to the first groove and the second groove extending parallel with the second groove and which extends at the side of the second groove facing away from the electronic component (GTP) to be mounted at a second distance from the latter electronic component which is less than the distance between the first groove and the second groove.

7. In re Claims 4 and 10, Shimada discloses the limitations of Claims 1-3 above, but does not explicitly disclose a distance between screw holes in an electronic component that is less than the distance between a first groove and a further groove. However, it would have been obvious to one having ordinary skill in the art of electronics at the time the invention was made to place a screw hole in a different location to correspond with a different groove location, since it has been held that rearranging parts of an invention involves only routine skill in the art. *In re Japikse*, 86 USPQ 70. The placement of a screw hole in a second location would make the electrical component more versatile to other mounting configurations.

8. Claims 5-8 and 11-15 are rejected under 35 U.S.C. 103(a) as being unpatentable over Shimada (Japanese Patent 08046381 A) (from IDS), Chalberg et al. (US Patent 7,191,998 B1) and further in view of Herzog (German Patent 252474 A1) (from IDS).

Art Unit: 2835

9. In re Claims 5 and 11, Shimada discloses an angle bracket (See Figures 1-2 and 4) that has a level base plate for placement against the mounting plate 100. Shimada does not disclose a clamping area angled off with respect to the base plate for a clamping fixation of the electronic component to be mounted. However, the use of such clamping angle brackets is common in the art of electronics. For example, Herzog discloses an angle bracket that has a level base plate 4 for placement against a mounting plate 2 and a clamping area 3 angled off with respect to the base plate 4 for a clamping fixation of a component 1 to be mounted. It would have been obvious to one having ordinary skill in the art of electronics at the time the invention was made to have substituted the angle bracket as disclosed in Shimada with the clamping angle bracket as disclosed by Herzog. All the claimed elements were known in the prior art and one skilled in the art could have combined the elements as claimed by known methods with no change in their respective functions, and the combination would have yielded predictable results to one of ordinary skill in the art at the time of the invention. See *KSR International Co. v. Teleflex Inc.*, 550 U.S. ____, 82 USPQ2d 1385 (2007). The use of a clamping angle bracket would better secure the electrical component to the mounting plate.

10. In re Claims 7 and 13, Shimada discloses nuts M1-M4 to accept screws N1-N4. Shimada does not disclose the nuts being spring nuts. The examiner takes official notice of facts outside the record, that spring nuts was well known in the art of electronics at the time of the invention, because of their ability to provide a more durable and shock absorbing lock with a screw. Therefore, it would have been obvious to a

Art Unit: 2835

person having ordinary skill in the art of electronics at the time of the invention to use spring nuts in place of the nuts disclosed by Shimada, in order to provide a more durable connection between the screws and the nuts.

11. In re Claims 8 and 14, Shimada discloses a first groove GP, a second groove GP and a further groove GP that are made of one piece with a plate 100 body.

12. In re Claim 15, Shimada discloses a mounting plate (100) for electronic components (GTR) having coolant lines (12, 14, 16, 18) integrated in a plate body (10) for a cooling fluid to flow through, wherein a fastening arrangement for mounting electronic components to be cooled, the mounting plate comprising: the plate body 10 comprising a first groove GP having a C-shaped cross section and extending in a straight line in an extension direction of the plate 10, into which at least one screwnut (M1-M4) for forming a screw (N1-N4) connection with an electronic component (GTR) can be inserted and fixed against relative rotation; and at least one second groove (GP) designed identically to the first groove (GP) and extending parallel with the first groove (GP) with a distance from the first groove (GP); and an angle bracket with a hole for mounting. Shimada does not explicitly disclose an angle bracket with elongated holes. However, the use of brackets with elongated holes to secure electrical devices is common in the art of electronics. For example, Chalberg discloses an angle bracket 30 that has an elongated hole 33 perpendicular to an extension direction for mounting an electronic device. It would have been obvious to one having ordinary skill in the art of electronics at the time the invention was made to have combined the bracket as disclosed in Chalberg with the mounting plate as disclosed by Shimada. All the claimed

Art Unit: 2835

elements were known in the prior art and one skilled in the art could have combined the elements as claimed by known methods with no change in their respective functions, and the combination would have yielded predictable results to one of ordinary skill in the art at the time of the invention. *See KSR International Co. v. Teleflex Inc.*, 550 U.S. ____, 82 USPQ2d 1385 (2007). The use of a bracket with elongated holes would allow for multiple mounting configurations. Shimada also does not disclose a clamping area angled off with respect to the base plate for a clamping fixation of the electronic component to be mounted. However, the use of such clamping angle brackets is common in the art of electronics. For example, Herzog discloses an angle bracket that has a level base plate 4 for placement against a mounting plate 2 and a clamping area 3 angled off with respect to the base plate 4 for a clamping fixation of a component 1 to be mounted. It would have been obvious to one having ordinary skill in the art of electronics at the time the invention was made to have substituted the angle bracket as disclosed in Shimada with the clamping angle bracket as disclosed by Herzog. All the claimed elements were known in the prior art and one skilled in the art could have combined the elements as claimed by known methods with no change in their respective functions, and the combination would have yielded predictable results to one of ordinary skill in the art at the time of the invention. *See KSR International Co. v. Teleflex Inc.*, 550 U.S. ____, 82 USPQ2d 1385 (2007). The use of a clamping angle bracket would better secure the electrical component to the mounting plate.

Response to Arguments

13. Applicant's arguments with respect to claims 1-15 have been considered but are moot in view of the new ground(s) of rejection above.

Conclusion

14. The Applicant should note that the Examiner has discovered prior art since the mailing of Examiner's first Office Action that is considered pertinent to applicant's disclosure. Ballard, Cianciotti and Hyser disclose a mounting apparatus similar to applicant's claimed invention.

15. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the date of this final action.

Art Unit: 2835

Any inquiry concerning this communication or earlier communications from the examiner should be directed to ADRIAN S. WILSON whose telephone number is (571)270-3907. The examiner can normally be reached on Mon.-Thu. 9am-5pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jayprakash Gandhi can be reached on (571) 272-3740. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Adrian S Wilson
Examiner
Art Unit 2835

Asw

/Jayprakash N Gandhi/
Supervisory Patent Examiner, Art Unit 2835